

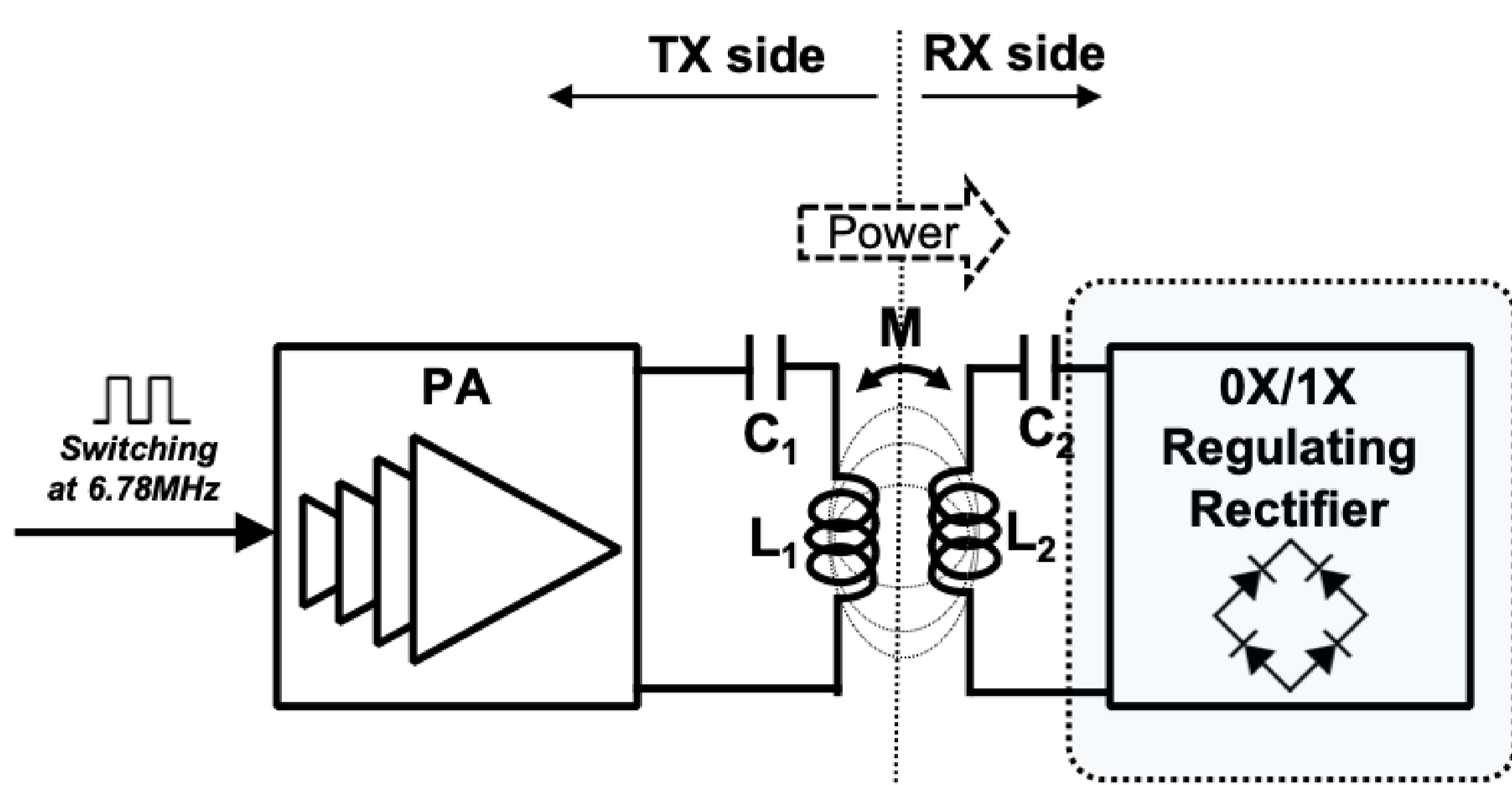
# Dual-Output Regulating Rectifier with Automatic Digital Offset Compensation

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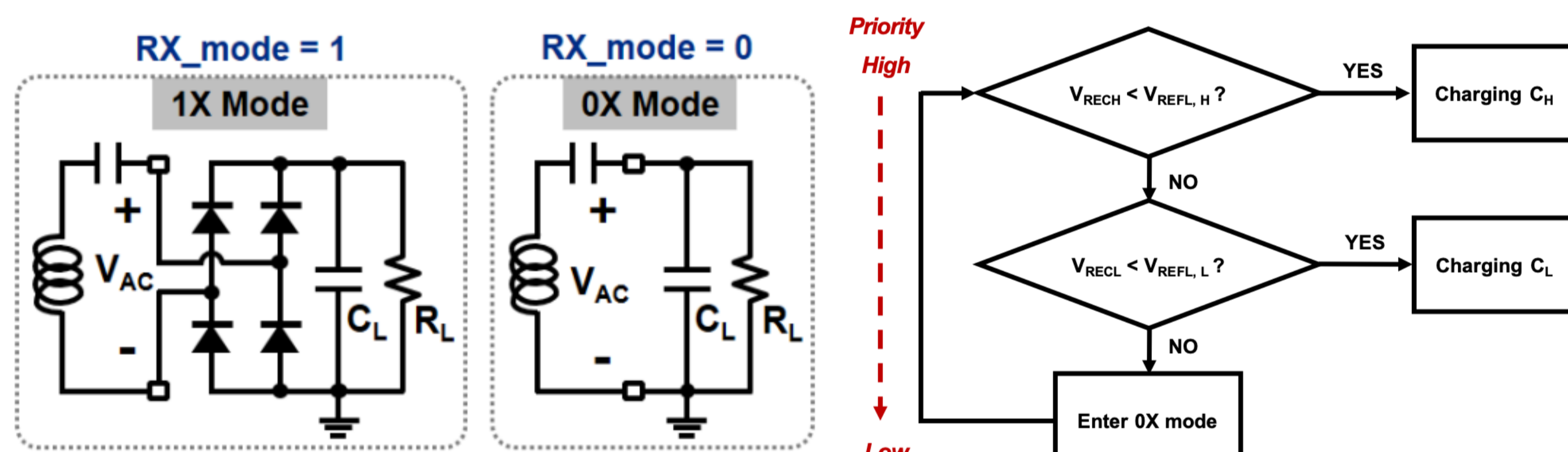
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## Introduction

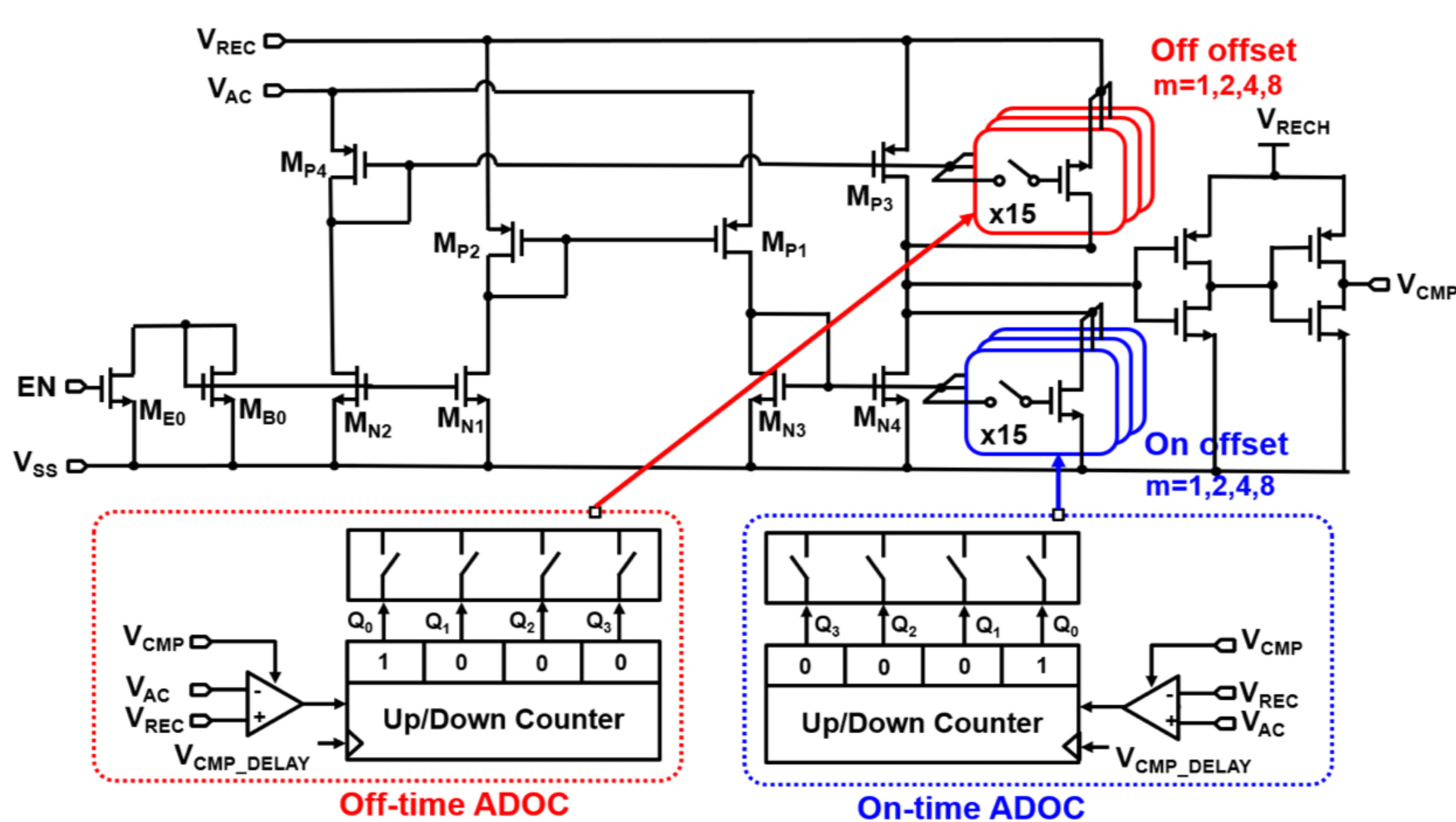
- The proposed regulating rectifier has the following characteristics
  - Dual outputs: 5V and 3.3V
    - Stimulating voltage and analog circuit power supply
  - 0X/1X regulating with PSM control
    - Provide regulated voltages for sub-circuits
  - Automatic digital offset compensation (ADOC)
    - On-time compensation: deal with insufficient conduction time
    - Off-time compensation: minimize reverse current



## Characteristics

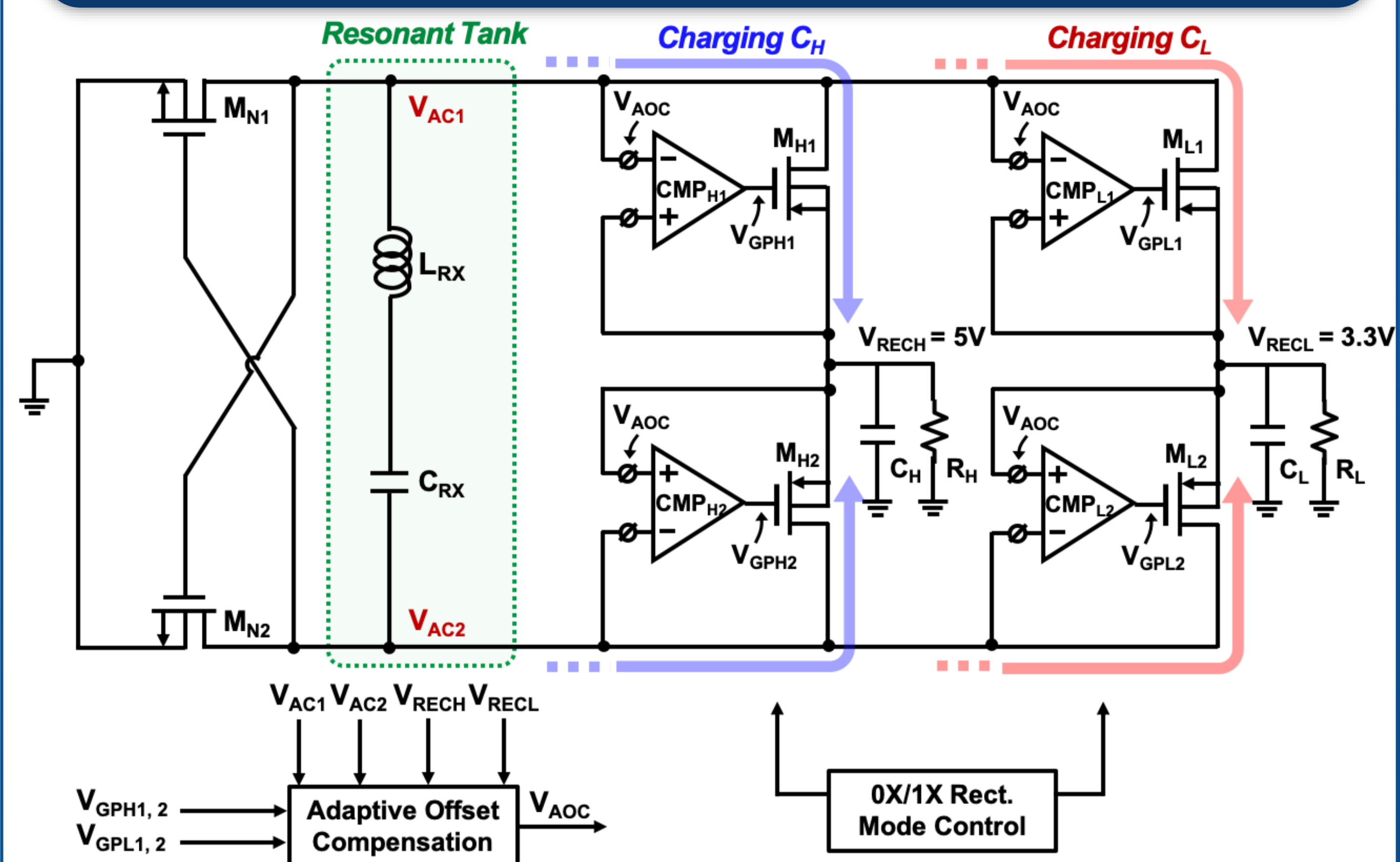


- By utilizing the PSM technique with hysteresis window, it can regulate desired output voltages.
- $V_{RECH}$  has higher priority than  $V_{RECL}$ , if both  $V_{RECH}$  and  $V_{RECL}$  are enough, system will enter 0X mode.



- This work realizes turn-on and turn-off offset compensation mechanism by comparing  $V_{REC}$  and  $V_{AC}$  and controls the offset current automatically.
- The compensation reaches an appropriate value through bit-by-bit calibration.

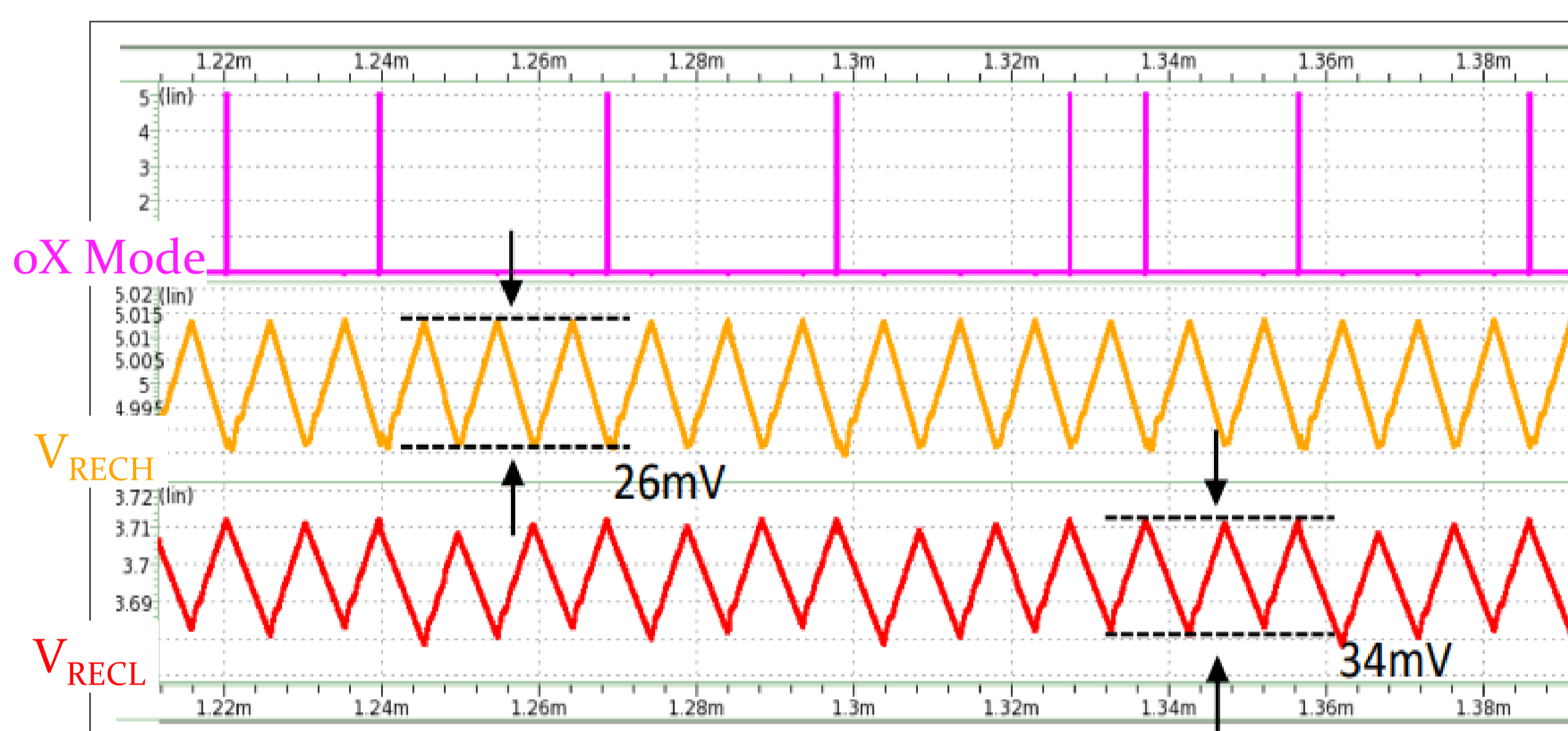
## System Architecture



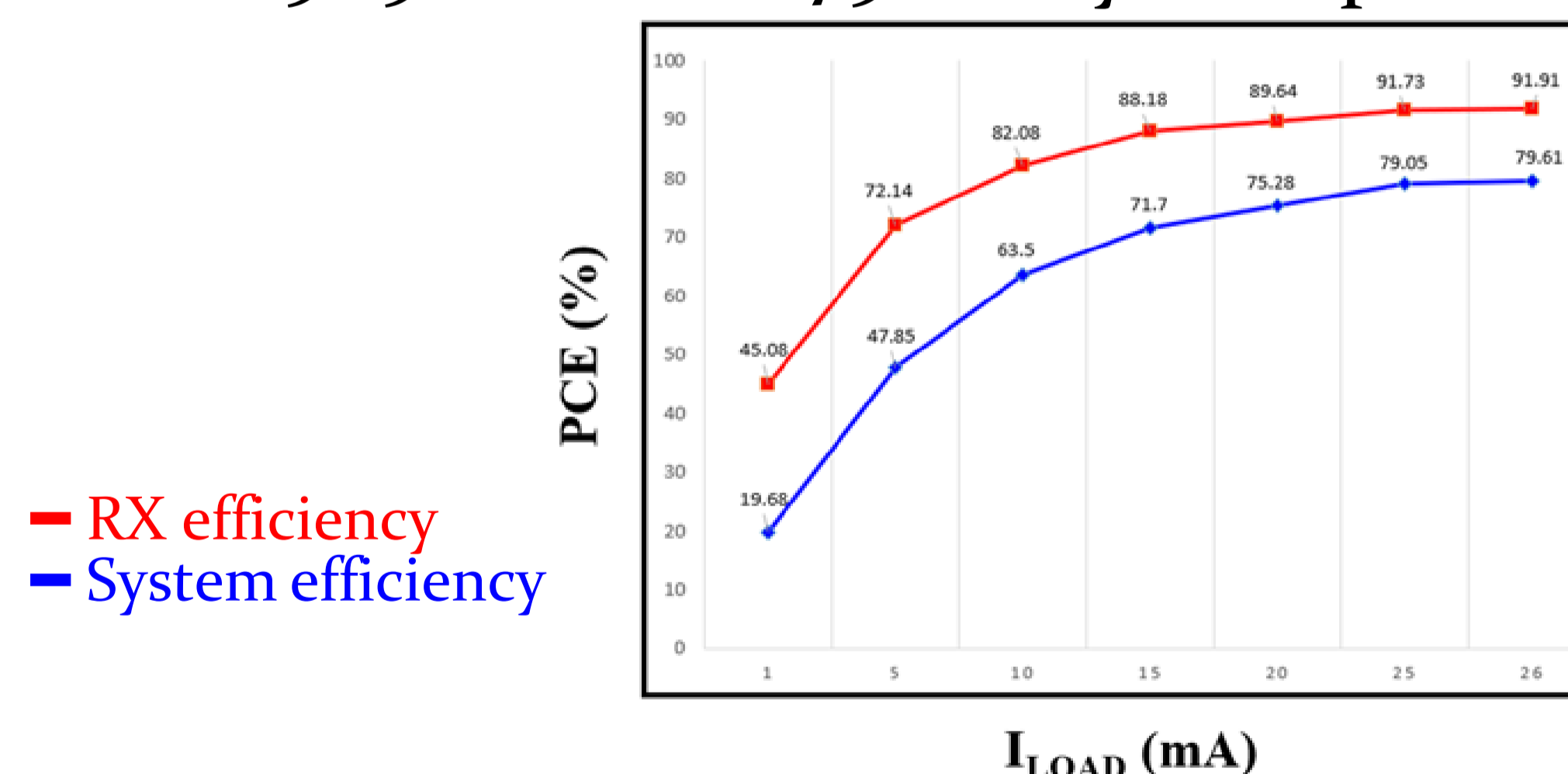
- One gate cross-coupled NMOS pair  $M_{N1}$  and  $M_{N2}$
- Two pair PMOS active diodes  $M_{H1}$ ,  $M_{H2}$  and  $M_{L1}$ ,  $M_{L2}$  with the comparator control.
- The resonant tank consists of  $L_{RX}$  and  $C_{RX}$  in series and it operates at 6.78-MHz

## Simulation Results

- Simulated waveform of dual-output voltages



- The proposed dual-output regulated WPT system achieves 91.9% Rx and 79.6% system peak efficiency.



## Reference

1. F.-B. Yang, J. Fuh, Y.-H. Li, M. Takamiya, and P.-H. Chen, "Structure-Reconfigurable Power Amplifier (SR-PA) and 0X/1X Regulating Rectifier for Adaptive Power Control in Wireless Power Transfer System," *IEEE J. Solid-State Circuits*, vol. 56, no. 7, pp. 2054–2064, Jul. 2021.
2. F.-B. Yang, J. Fuh, and P.-H. Chen, "A 13.56MHz wireless power transfer system with dual-output regulated active rectifier for implantable medical devices," in *Proc. IEEE 61st Int. Midwest Symp. Circuits Syst. (MWSCAS)*, Aug. 2018, pp. 440–443.

## Acknowledgement

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