

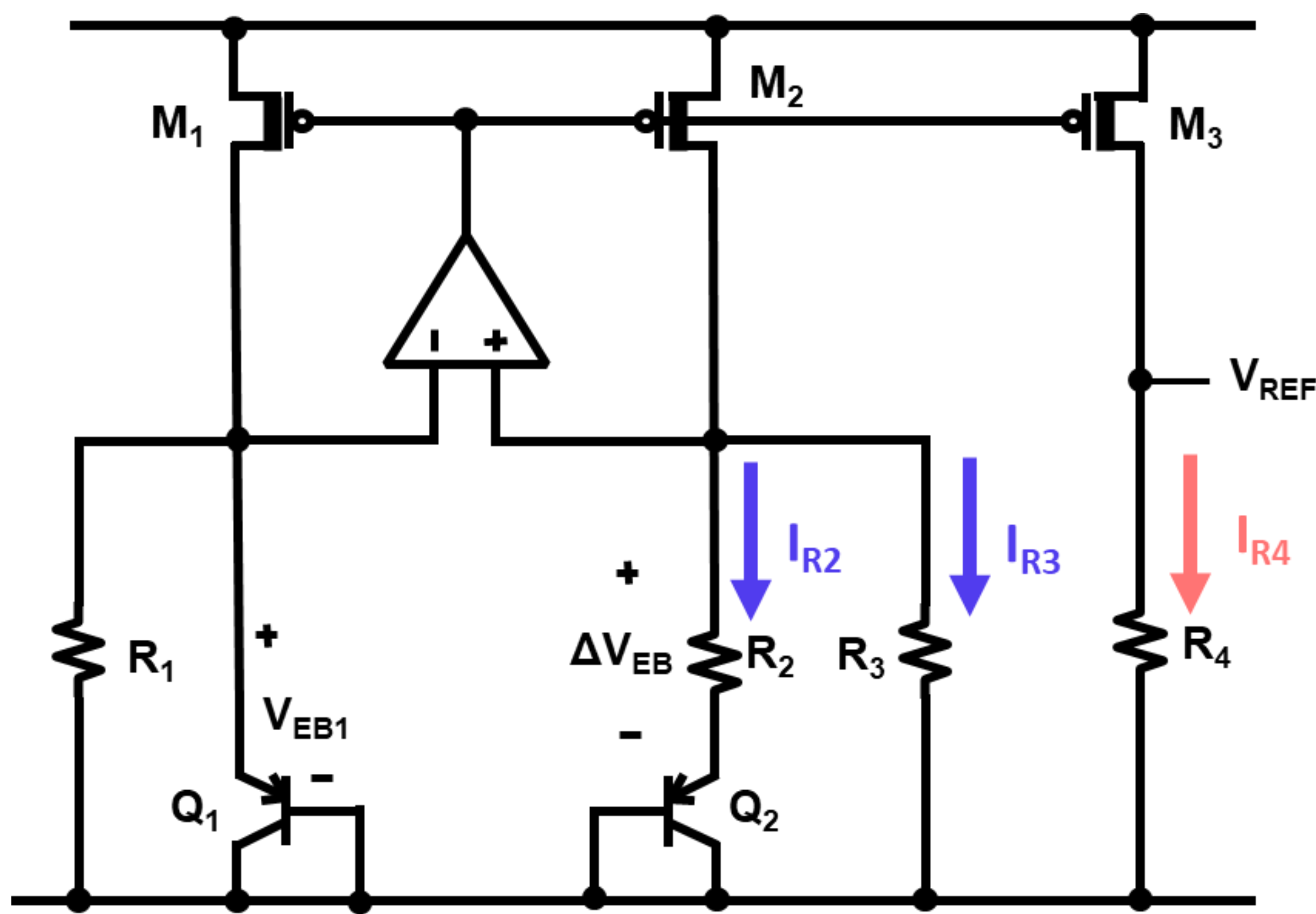
## Low-Power Bandgap Reference Design

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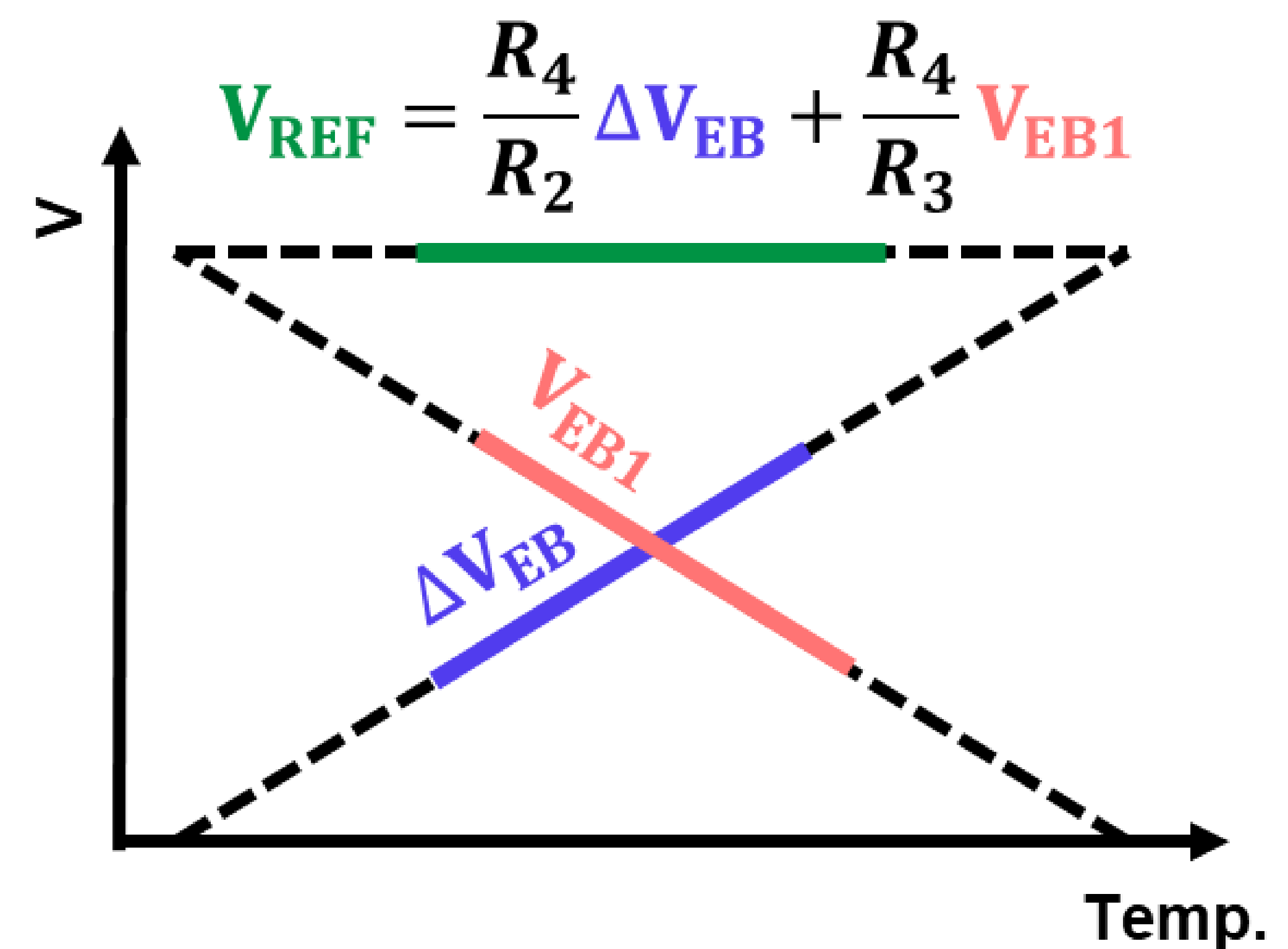
### Background

- A bandgap reference (BGR) circuit produces a constant voltage regardless of power supply variations, temperature changes, or circuit loadings.
- In order to meet the demand for reduced power consumption of circuit systems in recent years, the low-power BGR has become a major challenge.
- The supply voltage is limited by  $V_{EB} + V_{DS}$ . Since  $M_{1-3}$  should operate in saturation region,  $V_{EB}$  should be reduced to realize a sub-1V BGR.

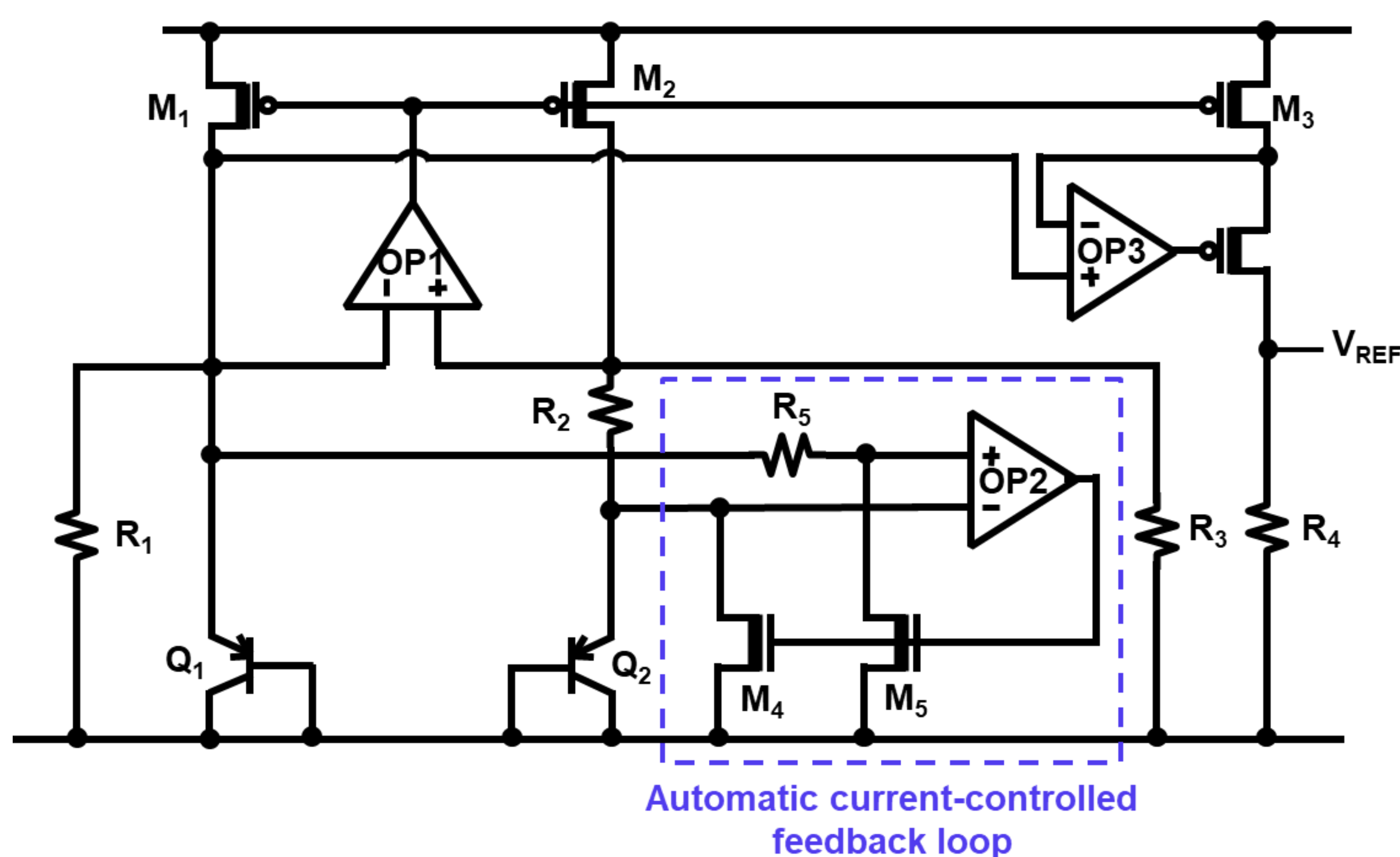


### Introduction

- The traditional BGR
  - $I_{R2}$  is a PTAT current. ( $I_{R2} = \frac{\Delta V_{EB}}{R_2}$ )
  - $I_{R3}$  is a CTAT current. ( $I_{R3} = \frac{V_{EB1}}{R_3}$ )
  - $I_{R4} = I_{R2} + I_{R3} = \frac{\Delta V_{EB}}{R_2} + \frac{V_{EB1}}{R_3}$
  - $V_{REF} = I_{R4} \times R_4 = \frac{R_4}{R_2} \Delta V_{EB} + \frac{R_4}{R_3} V_{EB1}$
  - $I_{R4}$  and  $V_{REF}$  are temperature-independent.



### Circuit Architecture (ISSCC 2019[1])



- The BGR is composed of a current-mode bandgap and an automatic current-controlled feedback loop.
- Decrease the current on BJTs to reduce  $V_{EB}$  by adding additional current paths ( $M_4$  and  $M_5$ ).
- The BJT current is controlled well by resistor ratio so as to let the BJT mismatch be fixed even for a lower bias-current range.

$$V_{REF} = I_{R4} \times R_4 = \left( \frac{\Delta V_{BE}}{R_2} + \frac{V_{EB}}{R_3} \right) R_4$$

$$\frac{I_{R3}}{I_{M5}} = \frac{\Delta V_{EB}/R_2}{\Delta V_{EB}/R_5} = \frac{R_5}{R_2}$$

- However, the extra operational amplifier (OP2) may increase power consumption and area, which degrades the system performance.

### Conclusion

- Sub-1V BGR circuits are necessary in many low-power applications.
- Since the supply voltage is limited mainly by  $V_{EB}$ , additional current paths are added to reduce the current on BJT, which reduce  $V_{EB}$  at the same time.
- However, using an extra opamp results in higher power consumption and larger area.

### Future Target

- We will develop new BGR architecture without using an extra opamp to reduce the current on BJTs and further decrease power consumption.
- The new BGR circuit will be applied in some low-power and low-supply-voltage applications, such as low-power energy-harvesting systems and thermal-sensors.

### Reference

- [1] Y. -W. Chen, J. -J. Horng, C. -H. Chang, A. Kundu, Y. -C. Peng and M. Chen, "18.7 A 0.7V, 2.35% 3 $\sigma$ -Accuracy Bandgap Reference in 12nm CMOS," 2019 IEEE International Solid-State Circuits Conference - (ISSCC), pp. 306-307, 2019.
- [2] B. Yousefzadeh, S. Heidary Shalmany and K. A. A. Makinwa, "A BJT-Based Temperature-to-Digital Converter With  $\pm 60$  mK (3 $\sigma$ ) Inaccuracy From  $-55$   $^{\circ}$ C to  $+125$   $^{\circ}$ C in 0.16- $\mu$ m CMOS," in IEEE Journal of Solid-State Circuits, pp. 1044-1052, April 2017.